

FIG.1B

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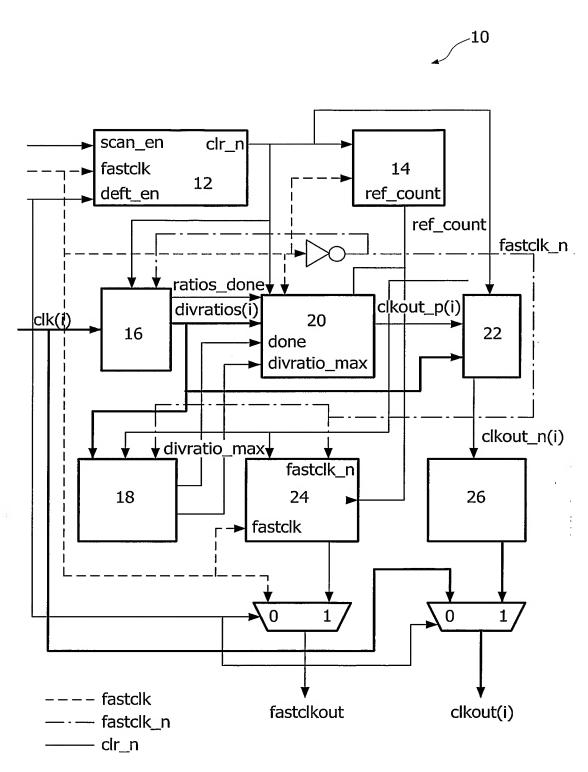
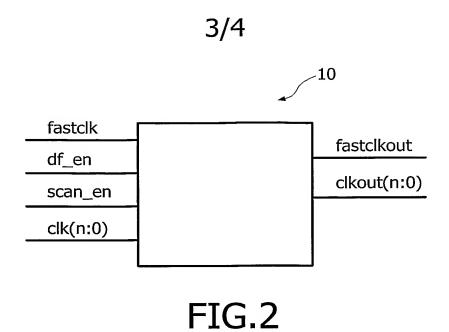


FIG.3

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enable_generator counter

00 \ 01 \ 10

enable_generator(LSB)

enable_generator(MSB)

divratio(i) counter

0 \ 1 \ 2 \ 3 \ 4 \ 5

FIG.4

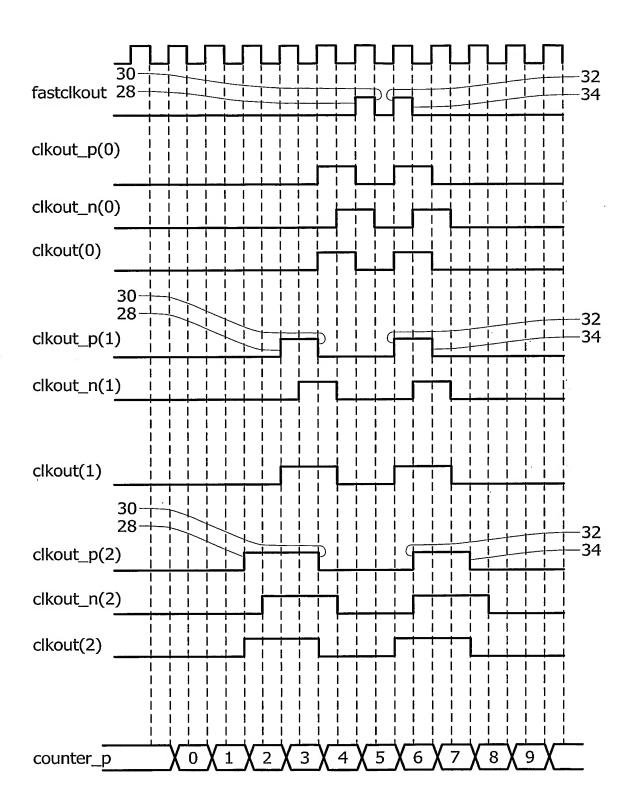


FIG.5